1   693244   interconnect\$3	L Number	Hits	Search Text	DB	Time stamp
1					2003/10/09 15:56
	-			US-PGPUB;	
11   specify\$3 same interconnect\$3 same component   SAPT,   US-PGPUB;   PRO, JPO;   DERWENT,   US-PGPUB;   PRO, JPO;				EPO; JPO;	
2   11   specify33 same interconnect\$3 same component   USPAT; same optimix\$5   Seame optimix\$5   Seame optimix\$5   Seame component same component same same integrated adj2 circuit) or (optimix\$3 adj4 integrated adj2 circuit) or (optimix\$3 adj4 integrate adj2 circuit) or (optimix\$3 adj4 sesimps\$3)) and (soft adj macro) and (soft adj macro) and (soft adj macro) or (firm adj macro) and (soft adj macro) or (firm adj macro) seame seame integrated adj2 circuit) or (optimix\$3 adj4 integrate adj2 circuit) or (optimix\$3 adj4 sesimps\$3) seame seame				DERWENT;	
Same optimis\$   US-POPUB;   EPO; JPO; DERMENT;   IBM TDB   USPAT;   US-POPUB;   EPO; JPO; DERMENT;   IBM TDB   USPAT;   US-POPUB;   EPO; JPO; DERMENT;   IBM TDB   USPAT;   US-POPUB;   EPO; JPO; DERMENT;   IBM TDB   EPO; JPO; DERMENT;   IBM TDB   EPO; JPO; DERMENT;   US-POPUB;   EPO; JPO; DERMENT;   US-POPUB;   EPO; JPO; DERMENT;   IBM TDB   EPO; JPO; DERMENT;   IBM TD	,			_	
Bep; JPC; DERMENT; IBM TOB   Same component same optimiz\$5   Same component same integrated adj2   Same component same integrated same component same same same same same same same same	2	11	specify\$3 same interconnect\$3 same component	USPAT;	2003/10/09 16:01
DERMENT;   Same component same optimiz\$5   Same interconnect\$3   Same component same optimiz\$5   Same component same component same optimiz\$5   Same component			same optimiz\$5	US-PGPUB;	
Sandwidth and (specify\$3 same interconnect\$3   IBM TDB   Same component same optimiz\$5   USPAT; US			_	EPO; JPO;	
3   6   bandwidth and (specify\$3 same interconnect\$3   USPĀT; same component same optimiz\$5   USPĀT; same component same integrated adj2 same component same integrated adj2 circuit) or (optimiz\$3 adj4 integr				DERWENT;	
Same component same optimiz\$5   US-PGPUB; EPO, JPO; DERNEMT; IEM TDB USPAT; US-PGPUB; EPO, JPO; DERNEMT; IEM					
Septimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)   Septimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj6 adj6 adj6 adj6 adj6 a	3	6	bandwidth and (specify\$3 same interconnect\$3		2003/10/09 16:02
5235   (optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)   DERNENT; IBM TDB USPAT; US-PGPUB; PRO, JPO, DERNENT; IBM TDB USPAT; US-PGP			same component same optimiz\$5 )		
5235   (optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)   USPAT; US-PGPUB; EPO, JPO, DERNEMT; IBM TDB USPAT; US-PGPUB; EPO, JPO, DERNEMENT; IBM TDB USPAT; US-PGPUB; EPO, JPO, JPO, DERNEMT; IBM TDB USPAT; US-PGPUB; EPO, JPO, JPO, DERNEMT; IBM TDB USPAT; US-PGPUB; E					
5235   (optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)   USPAT; US-PGPUB; EPO; JPO; DERWENT; imm TDB USPAT; US-PGPUB; EPO; JPO; DER				1	
Optimiz\$3 adj3 design\$3   US-POPUB; EPO; JPO; DERWENT; fBM TDB USPAT; US-POPUB; EPO; JPO; DERWENT; fBM TDB USPAT; US-POPUB; EPO; JPO; DERWENT; fBM TDB USPAT; US-POPUB; EPO; JPO; DERWENT; IBM TDB U				_	2222 /25 /22 27 22
Self-programmable adj3 integrated adj2   Self-programmable same integrated a	-	5235		1	2003/06/03 07:28
Derwent	.		(optimiz\$3 adj3 design\$3).	1	
Self-programmable adj3 integrated adj2					
Self-programmable adj3 integrated adj2					
Circuit			25 23 242 4-1		2002/06/02 14.54
Coptimiz\$3 adj4 integrated adj circuit) and ((optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj2 circuit) and ((self?programmable same integrated adj2 circuit) and ((self.adj macro) and ((hard adj macro)))	-	0			2003/00/02 14:54
Coptimiz\$3 adj4 integrated adj circuit) and ((optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj3 design\$3))			circuit		
194   (design\$3 adj4 integrated adj circuit) and ((optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj2 circuit) or (coptimiz\$3 adj4 integrated adj2 circuit) and ((soft adj macro) and ((hard adj macro)))   (design\$3 adj4 integrated adj2 circuit) and ((optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj circuit) and ((soft adj macro)))   (design\$3 adj4 integrated adj circuit) and ((soft adj macro)))   (design\$3 adj4 integrated adj circuit) and ((soft adj macro)))   (design\$3 adj4 integrated adj circuit) and ((soft adj macro)))   (coptimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$7 adj8 design\$3)) and ((soft adj macro) or (isp8 design\$3)) and (isp8 design\$3) and					
394   (design\$3 adj4 integrated adj circuit) and ((optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj3 design\$3))					*
((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3))  ((self?programmable same integrated adj2 circuit) and ((optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj circuit) and ((soft adj macro))  ((self adj macro) and ((hard adj macro) or (firm adj macro)))  ((optimiz\$3 adj4 integrated adj circuit) or (optimiz\$3 adj4 integrated adj circuit) and ((soft adj macro)))  ((soft adj macro)))  ((optimiz\$3 adj4 integrated adj circuit) and ((soft adj macro)))  ((optimiz\$3 adj4 integrated adj circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro) or (firm adj macro)))  ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))  8 self?programmable same integrated adj2 circuit)  - 8 self?programmable same integrated adj2  circuit  (soft adj macro) and ((hard adj macro) or (firm adj macro))  (soft adj macro) and ((hard adj macro) or (firm adj macro))  (soft adj macro) and ((hard adj macro) or (firm adj macro))  (soft adj macro) and ((hard adj macro) or (firm adj macro))  (soft adj macro) and ((hard adj macro) or (firm adj macro))  (soft adj macro) and ((hard adj macro) or (firm adj macro))  (soft adj macro) and ((hard adj macro) or (firm adj macro))  (soft adj macro) and ((hard adj macro) or (firm adj macro))  (soft adj macro) and ((hard adj macro) or (firm adj macro))  (soft adj macro) and ((hard adj macro) or (soft adj macro))  (soft adj macro) and ((hard adj macro) or (soft adj macro))  (soft adj macro) and ((hard adj macro) or (soft adj macro))  (soft adj macro) and ((hard adj macro) or (soft adj macro))  (soft adj macro) and ((hard adj macro) or (soft adj macro))  (soft adj macro) and ((hard adj macro) or (soft adj macro) and		20.	(decients add integrated add airmit) and	_	2003/06/02 14:57
(optimiz\$3 adj3 design\$3))  (self?programmable same integrated adj2 circuit) and ((soft adj macro) and ((hard adj macro)))  ((design\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj3 design\$3))) and ((soft adj macro)))  (design\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3))) and ((soft adj macro)))  (design\$3 adj4 integrated adj circuit) and ((soft adj macro)))  (design\$3 adj4 integrated adj circuit) and ((soft adj macro)))  (design\$3 adj4 integrated adj circuit) and ((soft adj macro)))  (design\$3 adj4 integrated adj circuit) or (firm adj macro)))  ((optimiz\$3 adj4 integrated adj circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))  8 self?programmable same integrated adj2 circuit) or (optimiz\$3 adj4 design\$3)) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))  8 self?programmable same integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj2 circui	-	394			2003/00/02 14.3/
				1	
Self?programmable same integrated adj2 circuit) and ((soft adj macro) and ((hard adj macro))   Self?programmable same integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj2 circuit) or (optimiz\$3 adj4 integrated adj circuit) and ((soft adj macro))   (design\$3 adj4 integrated adj circuit) and ((soft adj macro))   (design\$3 adj4 integrated adj circuit) and ((soft adj macro))   ((optimiz\$3 adj4 integrated adj2 circuit) and ((soft adj macro)))   ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro))   Self?programmable same integrated adj2   USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB   USPAT; US-PGPUB; EPO; JPO; DERWE	i i		(Optimiz\$3 adj3 design\$3/)		
0				1	
circuit) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))  - ((design\$3 adj4 integrated adj circuit) and ((optimiz\$3 adj3 design\$3))) and ((soft adj macro)))  - (optimiz\$3 adj4 integrated adj circuit) or (optimiz\$3 adj4 integrated adj circuit) and ((soft adj macro)))  - (design\$3 adj4 integrated adj circuit) and ((soft adj macro)))  - (design\$3 adj4 integrated adj circuit) and ((soft adj macro)))  - ((soft adj macro)) and ((hard adj macro))  - ((optimiz\$3 adj4 integrate adj2 circuit) or ((optimiz\$3 adj3 design\$3))) and ((soft adj macro)))  - ((optimiz\$3 adj4 integrate adj2 circuit) or ((optimiz\$3 adj3 design\$3))) and ((soft adj macro)))  - ((optimiz\$3 adj4 integrated adj2 circuit)  - ((optimiz\$3 adj4 integrated)  - (	'	_	(colforogrammable same integrated adia		2003/06/02 14:57
adj macro) or (firm adj macro)))  ((design\$3 adj4 integrated adj circuit) and ((optimiz\$3 adj3 design\$3))) and ((soft adj macro)))  (design\$3 adj4 integrated adj circuit) or (optimiz\$3 adj3 design\$3))) and ((soft adj macro)))  (design\$3 adj4 integrated adj circuit) and ((soft adj macro)))  (design\$3 adj4 integrated adj circuit) and ((soft adj macro)))  ((soft adj macro) and ((hard adj macro) or (firm adj macro)))  ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro)))  ((optimiz\$3 adj3 design\$3)) and ((soft adj macro)))  self?programmable same integrated adj2 circuit)  as self?programmable same integrated adj2 circuit)  circuit  self?programmable same integrated adj2 circuit)  as self?programmable same integrated adj2 circuit)  circuit  as self?programmable same integrated adj2 circuit)  circuit  self?programmable same integrated adj2 circuit)  self?programmable same integrated adj2 circu	1				2000,00,02 21101
DERMENT; IBM_TDB (logtimiz\$3 adj4 integrated adj circuit) and (logtimiz\$3 adj4 integrate adj2 circuit) or (logtimiz\$3 adj3 design\$3)) and (logt adj macro) and (logt adj macro) or (firm adj macro)))  design\$3 adj4 integrated adj circuit) and (logt adj macro))  design\$3 adj4 integrated adj circuit) and (logt adj macro))  (logtimiz\$3 adj4 integrated adj circuit) and (logt adj macro))  (logtimiz\$3 adj4 integrate adj2 circuit) or (logtimiz\$3 adj3 design\$3)) and (logt adj macro) and (logt adj macro))  align**  logtimiz\$3 adj4 integrated adj2 circuit) or (logtimiz\$3 adj3 design\$3)) and (logt adj macro) and (logt adj macro))  align**  logtimiz\$3 adj4 integrated adj2 circuit)  logtimiz\$4 adj6 acsign\$3)) and (logt adj6 adj6 acsign\$4)  logtimiz\$5 adj6 acsign\$3) and (logt adj6 acsign\$5)  logtimiz\$6 adj6 acsign\$6 adj6 acsign\$6 adj6 acsign\$7 ad			adi macro) or (firm adi macro))		
Composition of the properties of the propertie	1		adj macro, or (rirm adj macro,)		
-					
((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3))) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))  (design\$3 adj4 integrated adj circuit) and ((soft adj macro))  ((soft adj macro) and ((hard adj macro) or (firm adj macro)))  ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro))  ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro))  acro))  8 self?programmable same integrated adj2 circuit  circuit  8 self?programmable same integrated adj2 circuit  19 (soft adj macro) and ((hard adj macro) or (Ifirm adj macro))  10 circuit  11 class of the programmable same integrated adj2 circuit  12 circuit  13 coolono (soft adj macro) or (firm adj macro) or (Ifirm adj macro))  14 coolono (firm adj macro) or (Ifirm adj macro) or (Ifirm adj macro))  15 coolono (soft adj macro) or (Ifirm adj macro) or (Ifirm adj macro))  16 coolono (firm adj macro) or (Ifirm adj macro) or (Ifirm adj macro))  17 coolono (firm adj macro) or (Ifirm adj macro) or (Ifirm adj macro))  18 coolono (firm adj macro) or (Ifirm adj macro) or (Ifirm adj macro))  19 coptimiz\$3 and ((soft adj macro) and ((hard adj macro) or (Ifirm adj macro))		0	((design\$3 adi4 integrated adi circuit) and	_	2003/06/02 14:58
(optimiz\$3 adj3 design\$3)) and ((soft adj macro) and ((hard adj macro) or (firm adj macro))  (design\$3 adj4 integrated adj circuit) and ((soft adj macro))		, •			
macro) and ((hard adj macro) or (firm adj macro)))  (design\$3 adj4 integrated adj circuit) and ((soft adj macro)))  (firm adj macro))  ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro))  ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro))  8 self?programmable same integrated adj2 circuit  2003/06/02 1  8 self?programmable same integrated adj2 circuit  12467 design\$3 adj4 integrated adj circuit  125 pepuB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DE			(optimiz\$3 adi3 design\$3))) and ((soft adi	1	
macro)) (design\$3 adj4 integrated adj circuit) and (soft adj macro) and ((hard adj macro) or (firm adj macro)))  - 7 ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro)) - 8 ((hard adj macro) or (firm adj macro)) - 8 self?programmable same integrated adj2 circuit - 12467 design\$3 adj4 integrated adj circuit - 12467 design\$3 adj4 integrated adj2 circuit - 12467 design\$3 adj4			macro) and ((hard adj macro) or (firm adj	DERWENT;	•
((soft adj macro) and ((hard adj macro) or (firm adj macro)))  - ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))  - (soft adj macro) or (firm adj macro))  - (soft adj macro) or (firm adj macro))  - (soft adj macro) and ((hard adj adj2 macro))  - (soft adj macro) and ((hard adj adj2 macro))  - (soft adj macro) and ((hard adj macro))  - (soft adj macro) and ((hard adj macro))  - (soft adj macro)  - (soft	, ,			IBM_TDB	
((soft adj macro) and ((hard adj macro) or (firm adj macro)))  ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))  (self?programmable same integrated adj2 circuit)  (circuit)  (design\$3 adj4 integrated adj2 circuit)  (us-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EP	-	6	(design\$3 adj4 integrated adj circuit) and	USPAT;	2003/06/02 14:58
Topimiz\$3 adj4 integrate adj2 circuit			((soft adj macro) and ((hard adj macro) or	US-PGPUB;	
- ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))  - 8 self?programmable same integrated adj2 circuit  - 12467 design\$3 adj4 integrated adj circuit  - 12467 design\$3 adj4 integrated adj circuit  - 38 (soft adj macro) and ((hard adj macro) or (firm adj macro))  - 38 (soft adj macro) and ((hard adj macro) or (firm adj macro))  - 19 optimiz\$3 and ((soft adj macro) and ((hard Uspat; Ibm Tdb Uspat;			(firm adj macro)))		
- ((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and ((soft adj macro) and ((hard adj macro) or (firm adj macro))  - 8 self?programmable same integrated adj2 circuit  - 12467 design\$3 adj4 integrated adj circuit  - 12467 design\$3 adj4 integrated adj circuit  - 38 (soft adj macro) and ((hard adj macro) or (firm adj macro))  - 38 (soft adj macro) and ((hard adj macro) or (firm adj macro))  - 39 optimiz\$3 and ((soft adj macro) and ((hard use))  - 19 optimiz\$3 and ((soft adj macro) and ((hard use))  - 2003/06/02 1  - 2003/06/02 1		ļ			,
(optimiz\$3 adj3 design\$3)) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))  8 self?programmable same integrated adj2 USPAT; USPAT; USPGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US					
macro) and ((hard adj macro) or (firm adj macro))  8 self?programmable same integrated adj2	-	7			2003/06/02 15:04
macro)))  8 self?programmable same integrated adj2 circuit  12467 design\$3 adj4 integrated adj circuit  125 pGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;  19 optimiz\$3 and ((soft adj macro) and ((hard USPAT; 2003/06/02 1))	1 .				
- 8 self?programmable same integrated adj2 circuit  self?programmable same integrated adj2 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EP					
self?programmable same integrated adj2			macro)))		
circuit  US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB			100	_	2002/06/02 15:27
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; USPAT	-	8			2003/00/02 15:3/
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; U	'		circuit		
- 12467 design\$3 adj4 integrated adj circuit    IBM_TDB   USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB   USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB   USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB   EPO; JPO; DERWENT; IBM_TDB   EPO; JPO; DERWENT; IBM_TDB   USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB   USPAT; IBM_T					
- 12467 design\$3 adj4 integrated adj circuit USPĀT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPĀT; 2003/06/02 1					
US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT; (firm adj macro) and ((hard adj macro) or (firm adj macro))  US-PGPUB; EPO; JPO; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; 2003/06/02 1		12467	dogiants adid integrated adi girquit	_	2003/06/02 15:16
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; IBM_	-	1246/	designos adja integrated adj circuit	1	=====================================
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; IBM_TDB					
- 38 (soft adj macro) and ((hard adj macro) or (firm adj macro))  (firm adj macro))  - 19 optimiz\$3 and ((soft adj macro) and ((hard USPAT; 1BM_TDB USPAT; 1BM_TDB USPAT; 1BM_TDB USPAT; 2003/06/02 1					
- 38 (soft adj macro) and ((hard adj macro) or (1974); (firm adj macro)) USPĀT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPĀT; 19 optimiz\$3 and ((soft adj macro) and ((hard USPĀT; 2003/06/02 1))					
(firm adj macro))  US-PGPUB; EPO; JPO; DERWENT; IBM_TDB 19 optimiz\$3 and ((soft adj macro) and ((hard USPAT; 2003/06/02 1	-	3.8	(soft adi macro) and ((hard adi macro) or		2003/06/02 15:33
EPO; JPO; DERWENT; IBM_TDB  19 optimiz\$3 and ((soft adj macro) and ((hard USPAT; 2003/06/02 1					
DERWENT; IBM_TDB  19 optimiz\$3 and ((soft adj macro) and ((hard USPAT; 2003/06/02 1			1		
- 19 optimiz\$3 and ((soft adj macro) and ((hard USPAT; 2003/06/02 1					
- 19 optimiz\$3 and ((soft adj macro) and ((hard USPAT; 2003/06/02 1				1	
	-	19	optimiz\$3 and ((soft adj macro) and ((hard		2003/06/02 15:34
auj macro; or (lith auj macro;);     us-reros;			adj macro) or (firm adj macro)))	US-PGPUB;	
EPO; JPO;		1		EPO; JPO;	
DERWENT;	`	1			
IBM_TDB		<u></u>		IBM_TDB	

	-	0	((design\$3 adj4 integrated adj circuit) and ((optimiz\$3 adj4 integrate adj2 circuit) or	USPAT; US-PGPUB;	2003/06/02 15:38
			<pre>(optimiz\$3 adj3 design\$3))) and self?programmable</pre>	EPO; JPO; DERWENT; IBM_TDB	•
	- '	2	((optimiz\$3 adj4 integrate adj2 circuit) or	USPAT; US-PGPUB;	2003/06/02 15:38
	,		<pre>(optimiz\$3 adj3 design\$3)) and self?programmable</pre>	EPO; JPO;	
			•	DERWENT; IBM TDB	
		2	(design\$3 adj4 integrated adj circuit) and	USPAT; US-PGPUB;	2003/06/02 15:38
			self?programmable	EPO; JPO;	
				DERWENT; IBM_TDB	
	-	68	self?programmable	USPAT; US-PGPUB;	2003/06/02 15:39
			· ·	EPO; JPO;	
			*-	DERWENT; IBM_TDB	
	-	2447	design adj3 environment	USPAT; US-PGPUB;	2003/06/03 07:45
	•			EPO; JPO; DERWENT;	x
				IBM_TDB	2003/06/03 07:30
	-	274323	optimiz\$3	USPAT; US-PGPUB;	2003/06/03 07:30
				EPO; JPO; DERWENT;	÷
		660	(design adj3 environment) and optimiz\$3	IBM_TDB USPAT;	2003/06/03 07:30
	-	860	(design adj. environment) and optimizes	US-PGPUB;	2003,00,00 0.020
				EPO; JPO; DERWENT;	
	- ,	172	,   macro and ((design adj3 environment) and	IBM_TDB USPAT;	2003/06/03 07:30
	,		optimiz\$3)	US-PGPUB; EPO; JPO;	·
				DERWENT; IBM TDB	
		111	hard and (macro and ((design adj3	USPĀT;	2003/06/03 07:30
	*	•	environment) and optimiz\$3))	US-PGPUB; EPO; JPO;	
	·	,		DERWENT; IBM TDB	*
	-	47	soft and (hard and (macro and ((design adj3 environment) and optimiz\$3)))	USPAT; US-PGPUB;	2003/06/03 07:31
			environment, and opermizes,	EPO; JPO;	·
				DERWENT; IBM_TDB	
	-	1080	design adj environment	USPAT; US-PGPUB;	2003/06/03 07:49
	×			EPO; JPO; DERWENT;	
			(soft and macro) and ((hard adj macro) or	IBM_TDB USPAT;	2003/06/03 07:47
	-	54	(firm adj macro)	US-PGPUB;	2003,00,03 07.47
			,	EPO; JPO; DERWENT;	
	_	2	(design adj environment) and ((soft and	IBM_TDB USPAT;	2003/06/03 07:47
			macro) and ((hard adj macro) or (firm adj macro)))	US-PGPUB; EPO; JPO;	•
			100000	DERWENT;	
	-	28	(design adj environment) same optimiz\$3	IBM_TDB USPAT;	2003/06/03 07:49
	,			US-PGPUB; EPO; JPO;	. *
			*	DERWENT; IBM_TDB	
L		ľ	I	L	